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LSI Logic Corporation
Legal Department - IP
1621 Barber Lane, MS D-106
Milpitas, CA 95035

Date March 29, 2007

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To: US PTO

Mail Stop Status Request

TC 2182

Fax No. 571-273-8300

Phone No.

CC:

From: Connie del Castillo

Intellectual Property Paralegal

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No.

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REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please comment

Application Number: 10/711,783
Filing date: October 5, 2004
First named inventor: Ajitabh Saxena
Attorney docket number: 04-1551

Transmitted herewith for filing via facsimile:
• Status Request Letter

Pursuant to 37 C.F.R. 1.8, I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date indicated below:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of :
Ajitabh Saxena

Serial No.: 10/711,783

Group Art Unit: 2182

Filed: 10/5/2004

For: Method and System for Enforcing
Hardware/Software Compatibility
Constraints

Atty Docket: 04-1551

STATUS REPORT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

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1621 Barber Lane, MS D-106
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408-433-7475

Date: 27 MAR 07

Respectfully submitted,



Timothy Croll, Ph: [+1] 408-433-7191

Reg. No. 36,771